

L. S. College, Muzaffarpur.

ON LINE LECTURE NOTES
FOR. B. Sc. Physics Hon's

Paper - Electronics.

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Topic: - * Thevenin's Theorem.

State and prove Thevenin's Theorem

Any two terminal linear network containing energy sources (generators) and impedances can be replaced with an equivalent circuit consisting of a voltage source E' in series with an impedance Z' . The value of E' is the open circuit voltage between the terminals of the network and Z' is the impedance measured between the terminals of the network with all energy sources eliminated (but not their impedances). This is also called the voltage source equivalent circuit.

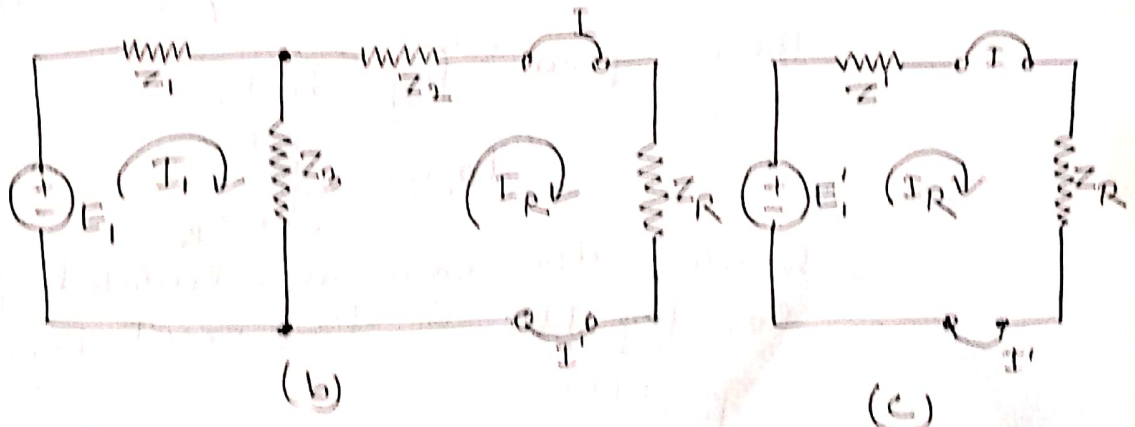
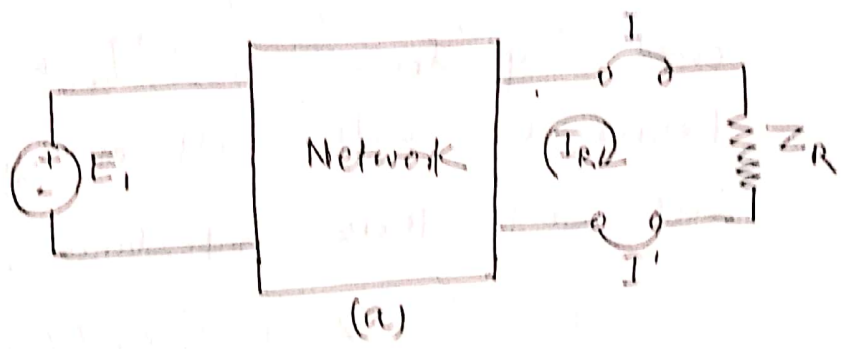


Fig: 1 Network illustrating Thevenin's theorem.

Fig. 1(b) is equivalent of fig. 1(a). From fig. 1(b);

$$E = I_1 (z_1 + z_3) - I_R z_3 \quad \text{--- (1)}$$

$$0 = -I_1 z_3 + I_R (z_2 + z_3 + z_R) \quad \text{--- (2)}$$

Then,
$$I_1 = I_R \frac{z_2 + z_3 + z_R}{z_3} \quad \text{--- (3)}$$

$$I_R = \frac{E z_3}{z_2 (z_1 + z_3) + z_1 z_3 + (z_1 + z_3) z_R} = \frac{E \left(\frac{z_3}{z_1 + z_3} \right)}{z_2 + \frac{z_1 z_3}{z_1 + z_3} + z_R} \quad \text{--- (4)}$$

From fig. 1(b), the open circuit voltage at I, I' terminals is

$$E' = E (z_3) / (z_1 + z_3) \quad \text{--- (5)}$$

and impedance measured between I, I' terminals, with all energy sources eliminated but not their impedances, is

$$z' = z_2 + z_1 z_3 / (z_1 + z_3) \quad \text{--- (6)}$$

Then, from fig. 1(c),

$$I_R = \frac{E'}{z' + z_R} \quad \text{--- (7)}$$

which is the same as calculated from fig. 1(b). So, fig. 1(c) is the exact equivalent of fig. 1(b).

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Example :- From the circuit in fig 2(a), draw the Thevenin's equivalent circuit.

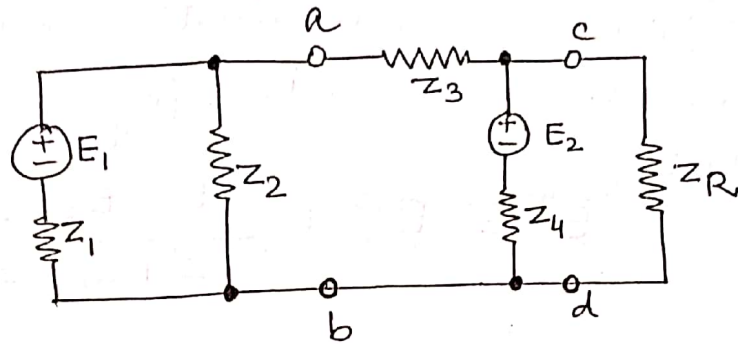
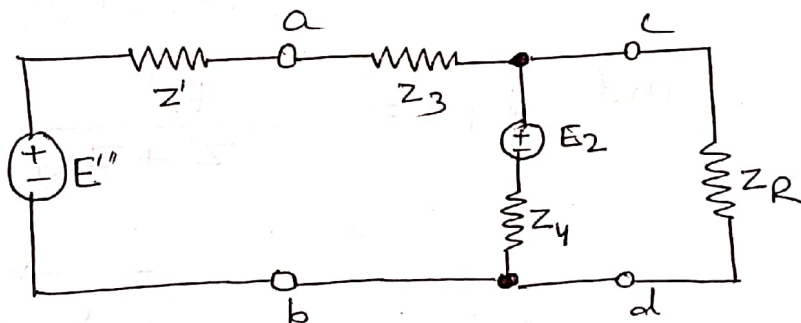
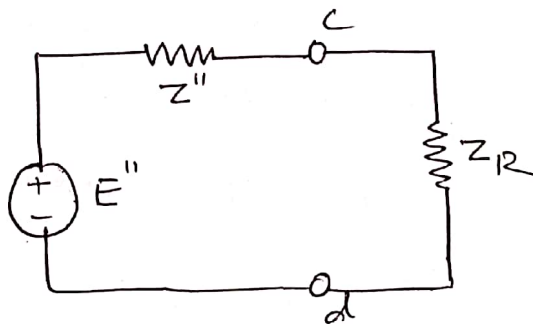


Fig - 2(a)



(b)



(c)

Fig- 2(a) Network of example (b) Thevenin equivalent at terminals a, b (c) Thevenin's equivalent at c, d .

The Thevenin equivalent circuit at terminals a, b is shown in fig - 2 (b), where

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$$E' = E_1 \left(\frac{z_2}{z_1 + z_2} \right)$$

$$z' = \frac{z_1 z_2}{z_1 + z_2}$$

Finally, the equivalent circuit at c, d terminals is shown in fig. (2) (c).

where,

$$E'' = E_2 + \frac{(E' - E_2) z_4}{z' + z_3 + z_4}$$

$$z'' = \frac{z_4 (z' + z_3)}{z_4 + z_3 + z'}$$

and $I_R = \frac{E''}{z'' + z_R}$

~~Ans~~