

Dr. Kumar Balwant Singh.

ON LINE LECTURE
NOTES FOR.

B. Sc. Part - II Physics (Hons)

Paper - 4-B.

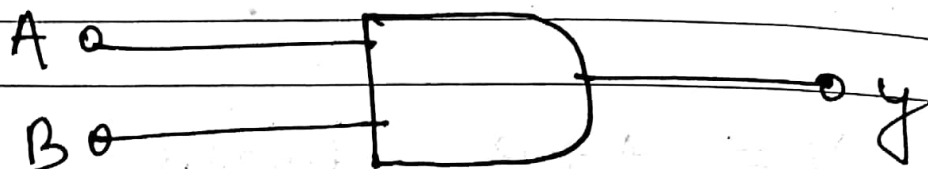
Topic - Basic Electronics.

KPS

ii) $y = A \cdot B$ implies y equals A AND B

A	B	y
0	0	0
0	1	0
1	0	0
1	1	1

i.e. y equals 1 if both A and B are 1.



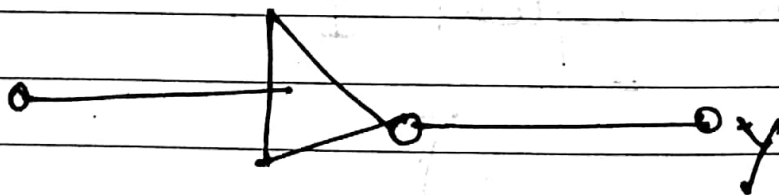
AND Gate

January
Thursday

15-351

iii) $y = \overline{A}$ \Rightarrow A' implies y equals NOT A

A	y
0	1
1	0



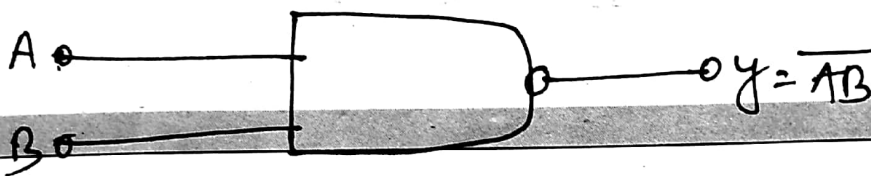
NOT or Inverter

iv) $y = \overline{AB} \Rightarrow y$ equals A NAND B

NAND means NOT AND

A	B	y
0	0	1
0	1	1
1	0	1
1	1	0

NAND means AND followed by inverter bubble (o) indicates inverter



v) $y = \overline{A+B} \Rightarrow y$ equals A NOR B

NOR means NOT OR

A	B	y
0	0	1
0	1	0
1	0	0
1	1	0

NOR means OR followed by NOT bubble (o) indicates inverter

